

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): KOBAYASHI, THOMAS GROUP ART UNIT: 2891
APPLN. NO.: 10/075218 EXAMINER: STEVEN J. FULK
FILED: 02-14-2002
TITLE: SEMICONDUCTOR DEVICE HAVING A FUSE AND METHOD OF FORMING
THEREOF

Certificate of Submission

I hereby certify that this correspondence is being submitted to the
USPTO, Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States
Postal Service with sufficient postage as first class mail.
☐ Facsimile transmitted in accordance with C.F.R. § 1.6(d).
☒ Submitted electronically via EFS in accordance with "Legal
Framework for EFS Web".

March 28, 2008
Date of Submission

/Dee Matocha/
Signature

Dee Matocha
Printed Name of Person Signing Certificate

**NOTICE OF APPEAL FROM THE EXAMINER TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

In accordance with 37 CFR 41.31, applicants hereby appeal to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 503079, Freescale Semiconductor, Inc.

Respectfully submitted,

FREESCALE SEMICONDUCTOR, INC.
Customer Number: 23125

/Kim-Marie Vo/
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